Ian Camaclang

(street address upon request) **(858) 337-0991**
San Diego, CA 92131 **ian@arcanegel.com** **http://www.arcanegel.com/**

Experience:

Peregrine Semiconductor, San Diego, CA

Sr. Automation Engineer **May 2014 to present**
ATE Software Engineer **May 2012 to May 2014**

RF software design and development for automated test equipment (ATE):

- Design, programming, and troubleshooting ATE software for RFIC products in LabVIEW, CVI, Visual Studio, and TestStand.
- Project management and development of software infrastructure for standard in-house test racks.
- Project management and development of driver libraries, vector file format, and user interfaces for in-house digital hardware.
- Development of class drivers for third-party RF and digital instruments.
- Source control of test software in SVN.
- Data analysis and scripting in Excel and JMP.
- Software development documentation in Word, Excel, and PowerPoint.

Northrop Grumman, San Diego, CA.

RF Engineer **Jun 2002 to Apr 2012**
Graduate Intern **Apr 2001 to Jun 2002**

RF and digital test automation programming using LabVIEW, CVI, and TestStand on military radios:

- RF and digital test programming on the Lockheed Martin LM-STAR rack with CVI and TestStand on modules for the F-35 Joint Strike Fighter program. RF test programming on the Raytheon Common Test Station with LabVIEW and TestStand on modules and radios for the AMF JTRS program.
- RF test programming using standalone LabVIEW programs on modules and radios for F-35 and on modules for AMF.
- GPIB instrument programming and manual RF bench testing for F-35 and AMF module development using standard RF instruments.
- Source control of test software in ClearCase.
- Strict compliance with security requirements in unclassified, classified, and special access areas of F-35 and AMF.
- Engineering and software development documentation in Word, Excel, and PowerPoint.

RF design support for Nokia chips and radio products for internal research and development:

- RF chip layout design in Microwave Office for RFICs for Nokia base stations and in Cadence for RFICs for internal R&D.
- RF PCB layout in Orcad Layout for products in internal R&D and for boards testing RFICs for Nokia.
- Administration of foundry design kits in Windows, Unix, HTML for RFICs in internal R&D.

Skills:

- RF Test Engineering: National Instruments LabWindows/CVI, LabVIEW, and TestStand on standard RF instruments.
- Script Programming: Perl, Python, UNIX Bash shell, and DOS batch files.
- General Programming: Java/SWT in Eclipse, Visual Studio, C/C++, and Sun SPARC Assembly.
- Source Control: IBM/Rational ClearCase and Tortoise SVN.
- System Administration: DOS, Windows, and UNIX, including Linux, Cygwin, and Mac OS X.
- RF Layout: AWR Microwave Office, Cadence, Orcad Capture and Orcad Layout.
- Data Analysis: Microsoft Excel with VBA, JMP, and MATLAB.
- WWW Authoring: Standard-compliant HTML/XHTML, CSS, Perl, and PHP. Apache Web Server administration.
- Desktop Publishing: Microsoft Office, OpenOffice.org, Adobe PageMaker, Adobe Photoshop, and The GIMP.

Degrees and Certifications:

- Engineer-in-Training, CA.
- M.Eng. (Master of Engineering) in Electrical Engineering at the University of California at San Diego, La Jolla, CA.
- B.S. in Bioengineering at the University of California at San Diego, La Jolla, CA.